

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	METHOD AND STRUCTURE FOR CONNECTING GROUND/POWER NETWORKS TO PREVENT CHARGE DAMAGE IN SILICON ON INSULATOR
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Application Number: 10/709,325

Confirmation Number:

First Named Applicant: Kenneth DeVries

Attorney Docket Number: BUR920030184US1

Art Unit: 2811

Examiner: Jung hwa Im

Search string: ( 5587598 or 5691234 or 5817577 or 6034433 or 6194233 or 6410964 or 6417544 or 6420208 or 20020022328 or 20020094625 or 20020098604 or 20020142526 or 20020197776 or 20030080385 ).pn

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

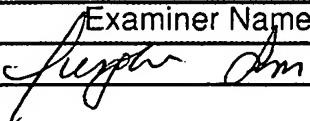
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
Jmz	1	5587598	1996-12-24	Hatanaka			
	2	5691234	1997-11-25	Su, et al			
	3	5817577	1998-10-06	Ko			
	4	6034433	2000-03-07	Beatty			
	5	6194233	2001-02-27	Bedner, et al			
	6	6410964	2002-06-25	Shida			
	7	6417544	2002-07-09	Jun, et al			
✓	8	6420208	2002-07-16	Pozder, et al			

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
Jmz	1	20020022328	2002-02-21	Ang, et al			
	2	20020094625	2002-07-18	Hashimoto, et al			
	3	20020098604	2002-07-25	Lin, et al			
	4	20020142526	2002-10-03	Khare, et al			
	5	20020197776	2002-12-26	Zhang, et al			
✓	6	20030080385	2003-05-01	Kimura			

**Signature**

Examiner Name	Date
	Jan 5, 2006